

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Conf. No.:

1358

Theodore W. Houston

Docket No.:

TI-28032.1

Serial No.: 10/085,646

Examiner: Berry, Renee R.

Filed:

02/27/2002

Art Unit:

2818

For:

Semiconductor Device with Fully Self-Aligned Local Interconnects, and

Method for Fabricating the Device

## Response under 37 CFR 1.111

Commission for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 06/18/03. They are respectfully submitted as a full and complete response to that Action. The Commissioner is hereby requested and authorized to charge any fees necessary for the filing of the enclosed papers to deposit account number 20-0668 of Texas Instruments Incorporated.